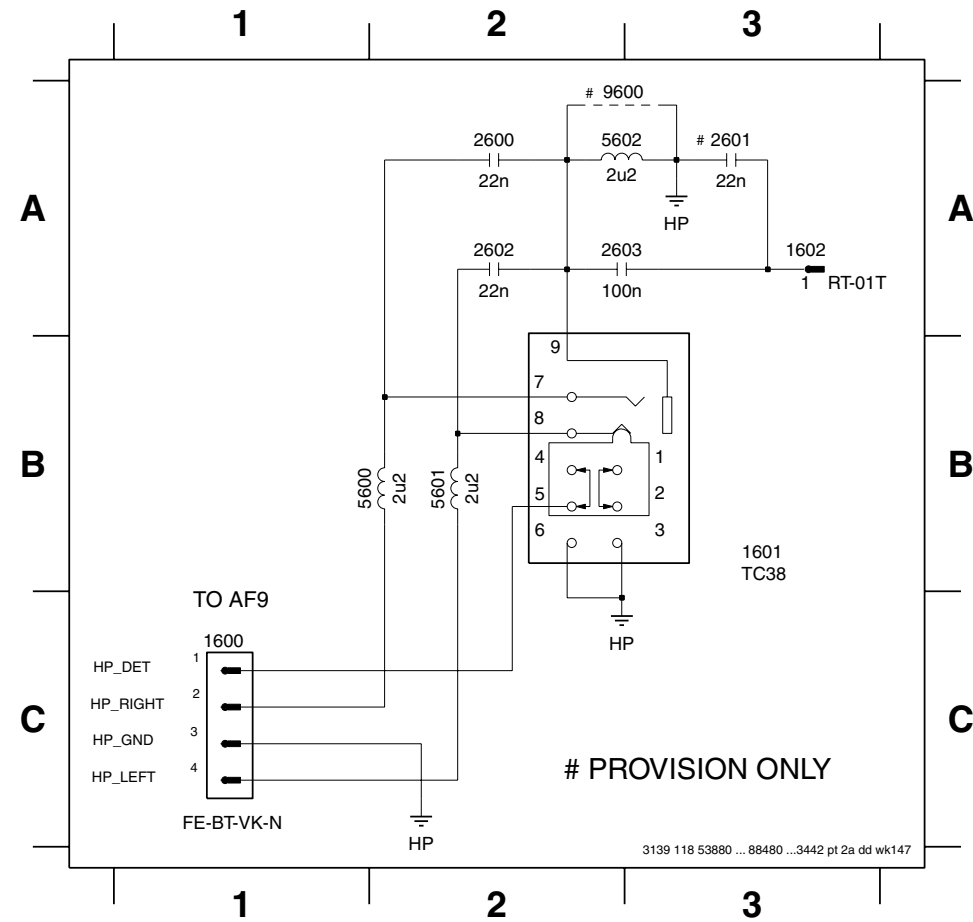


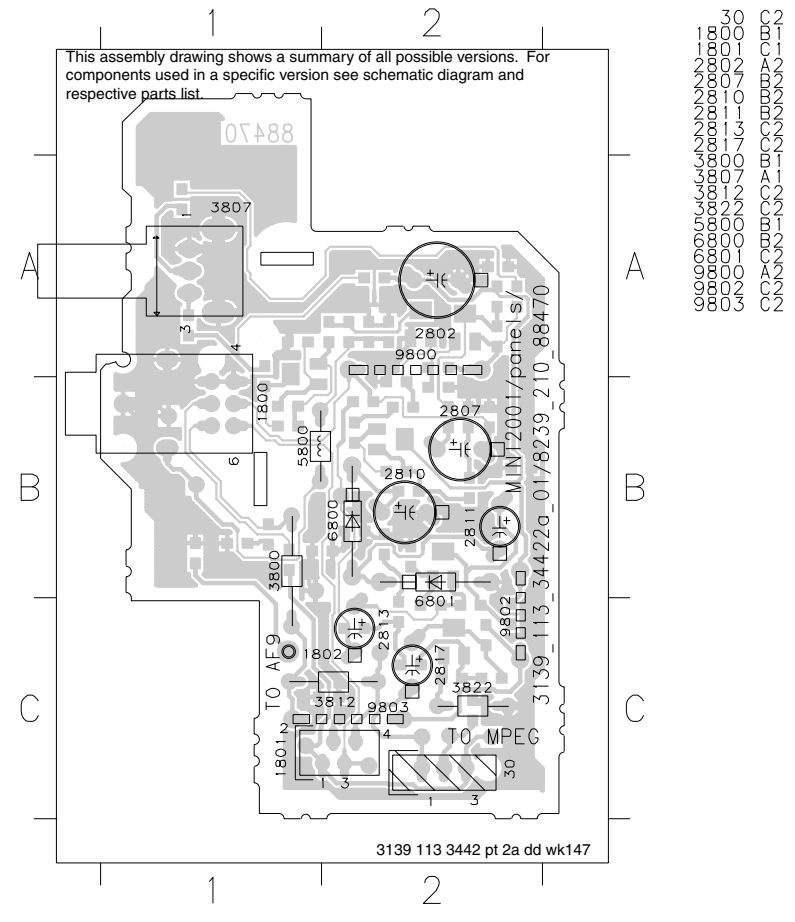


# HEADPHONE PART - CIRCUIT DIAGRAM



- 1600 C1
- 1601 B3
- 1602 A3
- 2600 A2
- 2601 A3
- 2602 A2
- 2603 A2
- 5600 B2
- 5601 B2
- 5602 A2
- 9600 A2

# KARAOKE BOARD - COMPONENT & CHIP LAYOUT

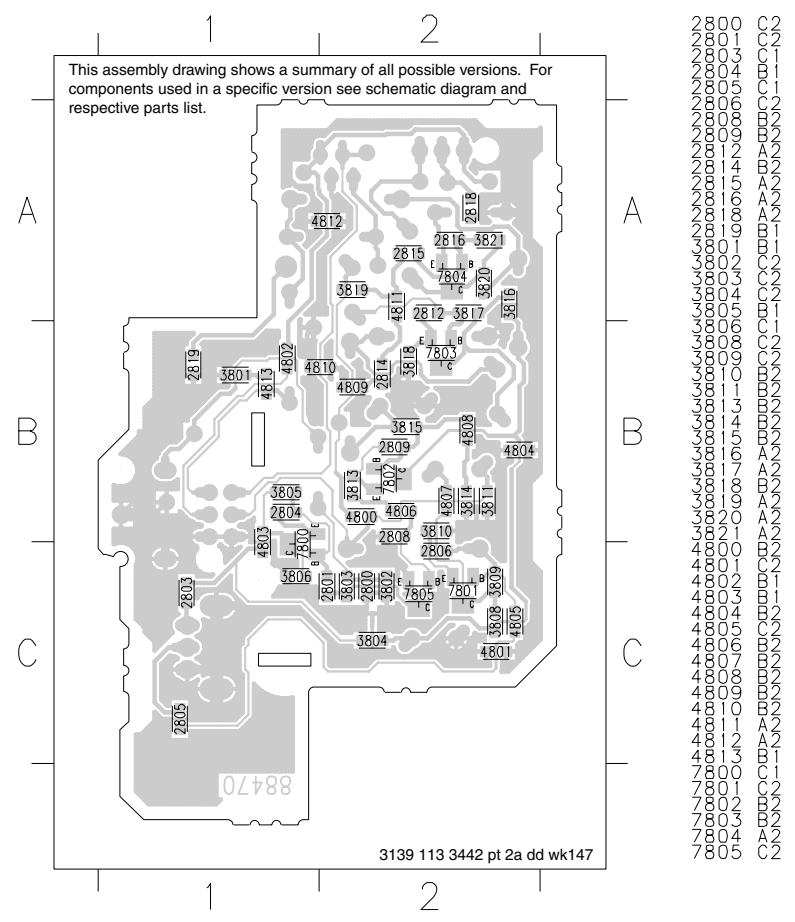
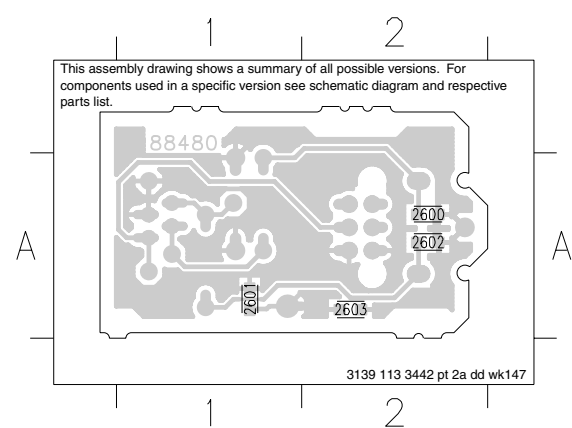
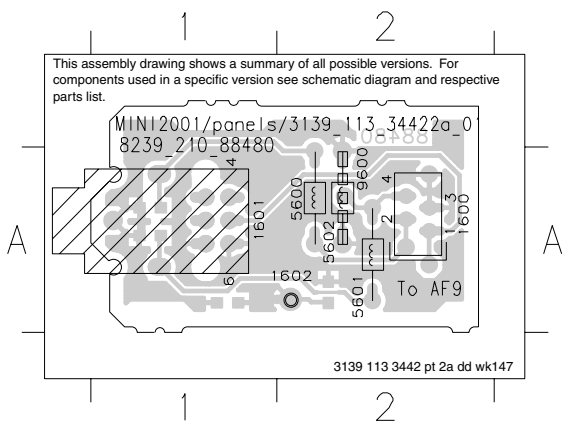


- 1600 C1
- 1601 B3
- 1602 A3
- 2600 A2
- 2601 A3
- 2602 A2
- 2603 A2
- 5600 B2
- 5601 B2
- 5602 A2
- 9600 A2

# HEADPHONE BOARD - COMPONENT & CHIP LAYOUT

- 1600 A2
- 1601 A1
- 5600 A2
- 5601 A2
- 5602 A2
- 9600 A2

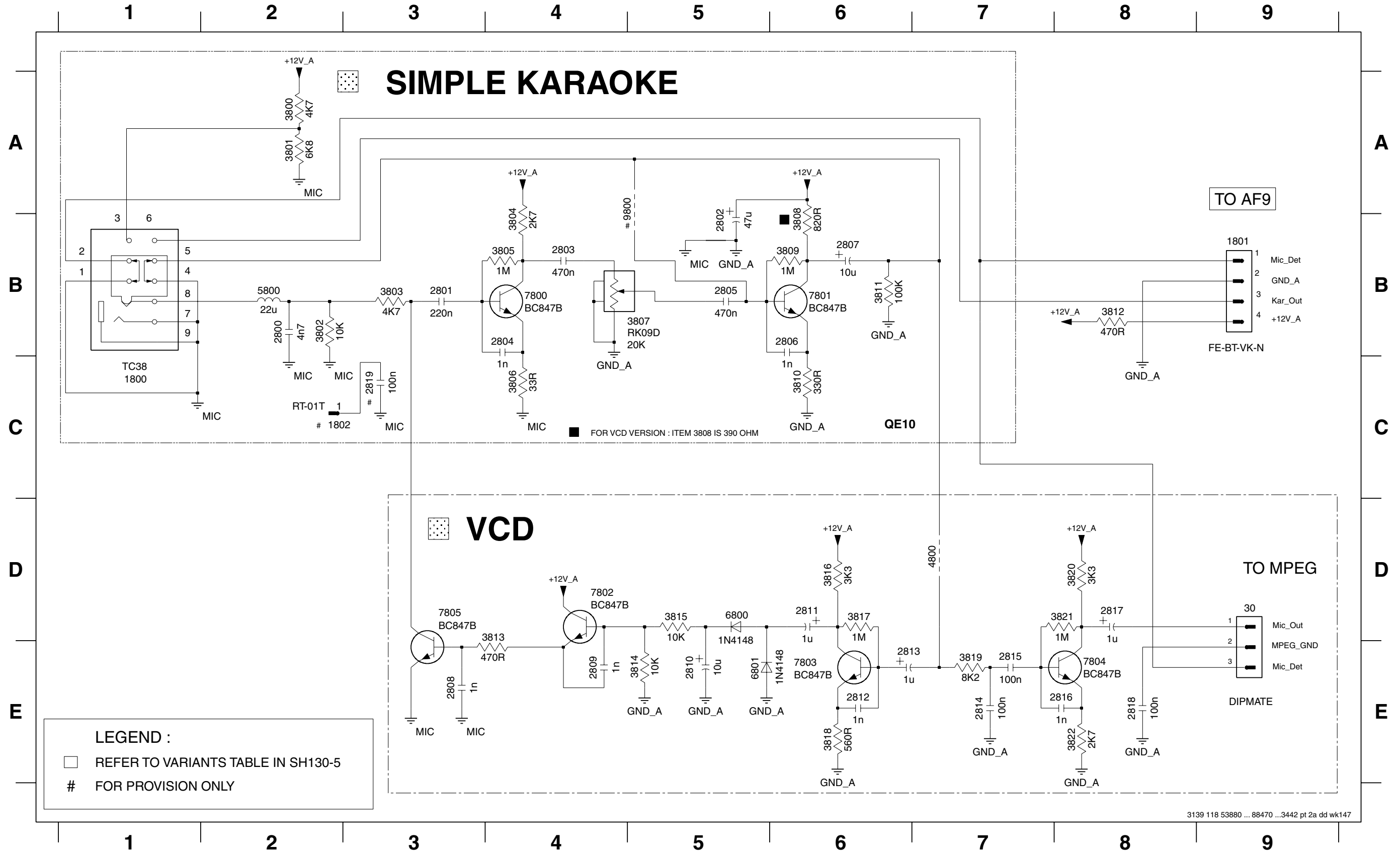
- 2600 A2
- 2601 A1
- 2602 A2
- 2603 A2



- 1600 C1
- 1601 B3
- 1602 A3
- 2600 A2
- 2601 A3
- 2602 A2
- 2603 A2
- 5600 B2
- 5601 B2
- 5602 A2
- 9600 A2

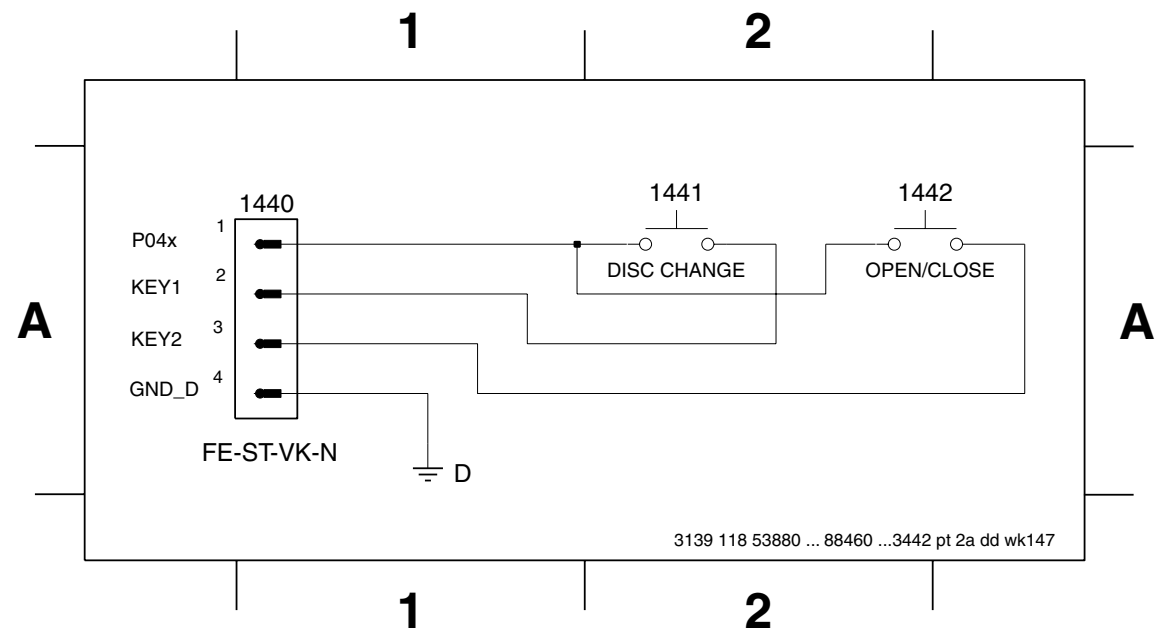
# KARAOKE PART - CIRCUIT DIAGRAM

30 D9	1802 C2	2802 B5	2805 B5	2808 E3	2811 D6	2814 E7	2817 D8	3800 A2	3803 B3	3806 C4	3809 B6	3812 B8	3815 D5	3818 E6	3821 D8	5800 B2	7800 B4	7803 E6	9800 A5
1800 C1	2800 B2	2803 B4	2806 B6	2809 E4	2812 E6	2815 E7	2818 E8	3801 A2	3804 B4	3807 B4	3810 C6	3813 D4	3816 D6	3819 E7	3822 E8	6800 D5	7801 B6	7804 E8	
1801 B9	2801 B3	2804 B4	2807 B6	2810 E5	2813 E6	2816 E8	2819 C3	3802 B2	3805 B4	3808 B6	3811 B6	3814 E5	3817 D6	3820 D8	4800 D7	6801 E5	7802 D4	7805 D3	



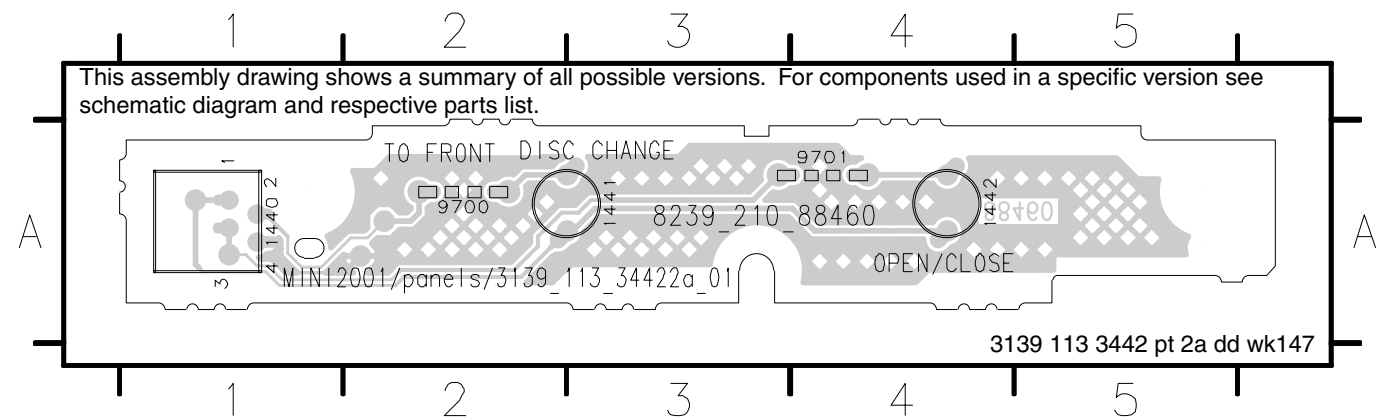
## KEY-CDC PART - CIRCUIT DIAGRAM

1440 A1 1441 A2 1442 A2

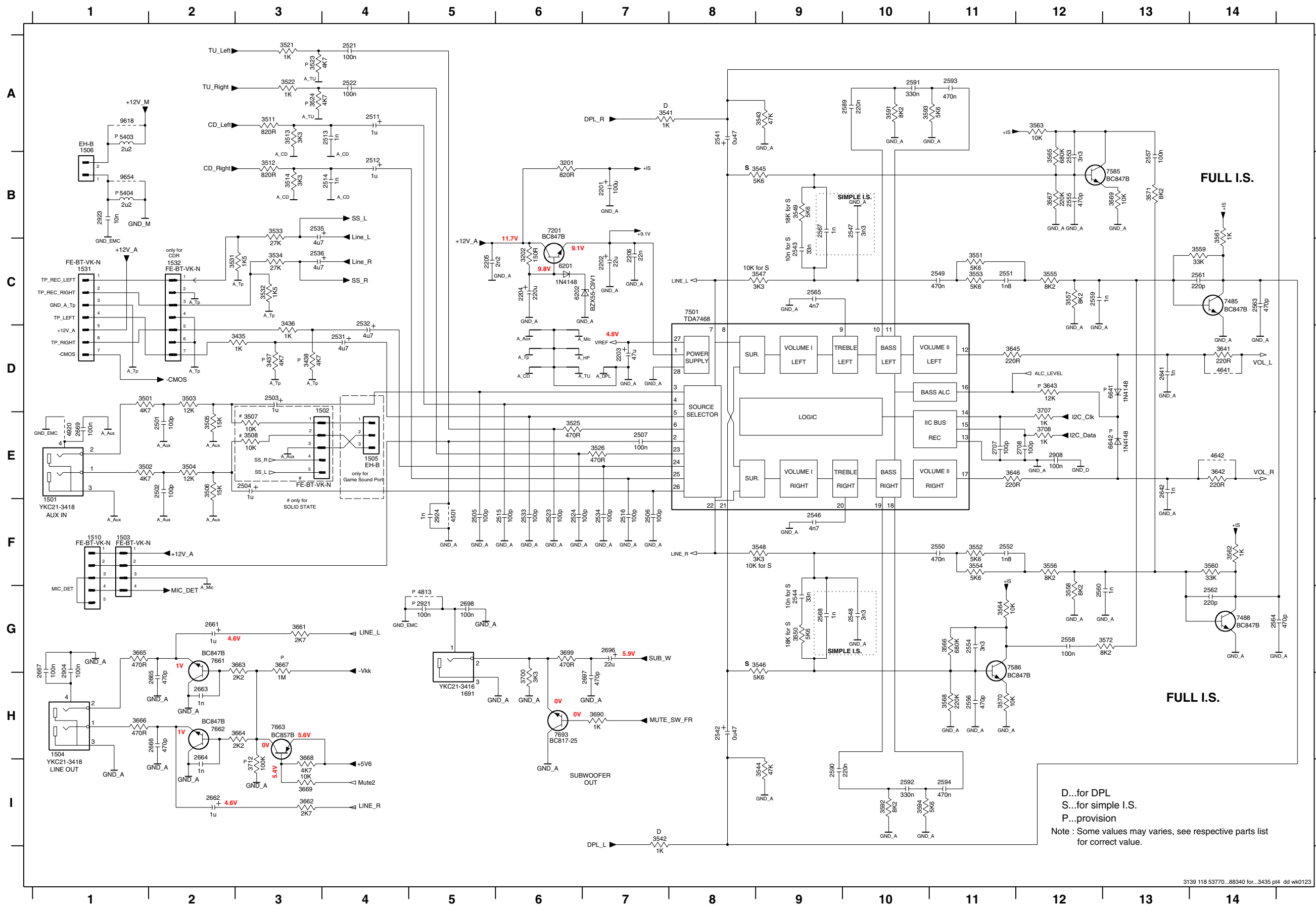


## KEY-CDC BOARD - COMPONENT LAYOUT

1440 A1 1441 A3 1442 A4 9700 A2 9701 A4

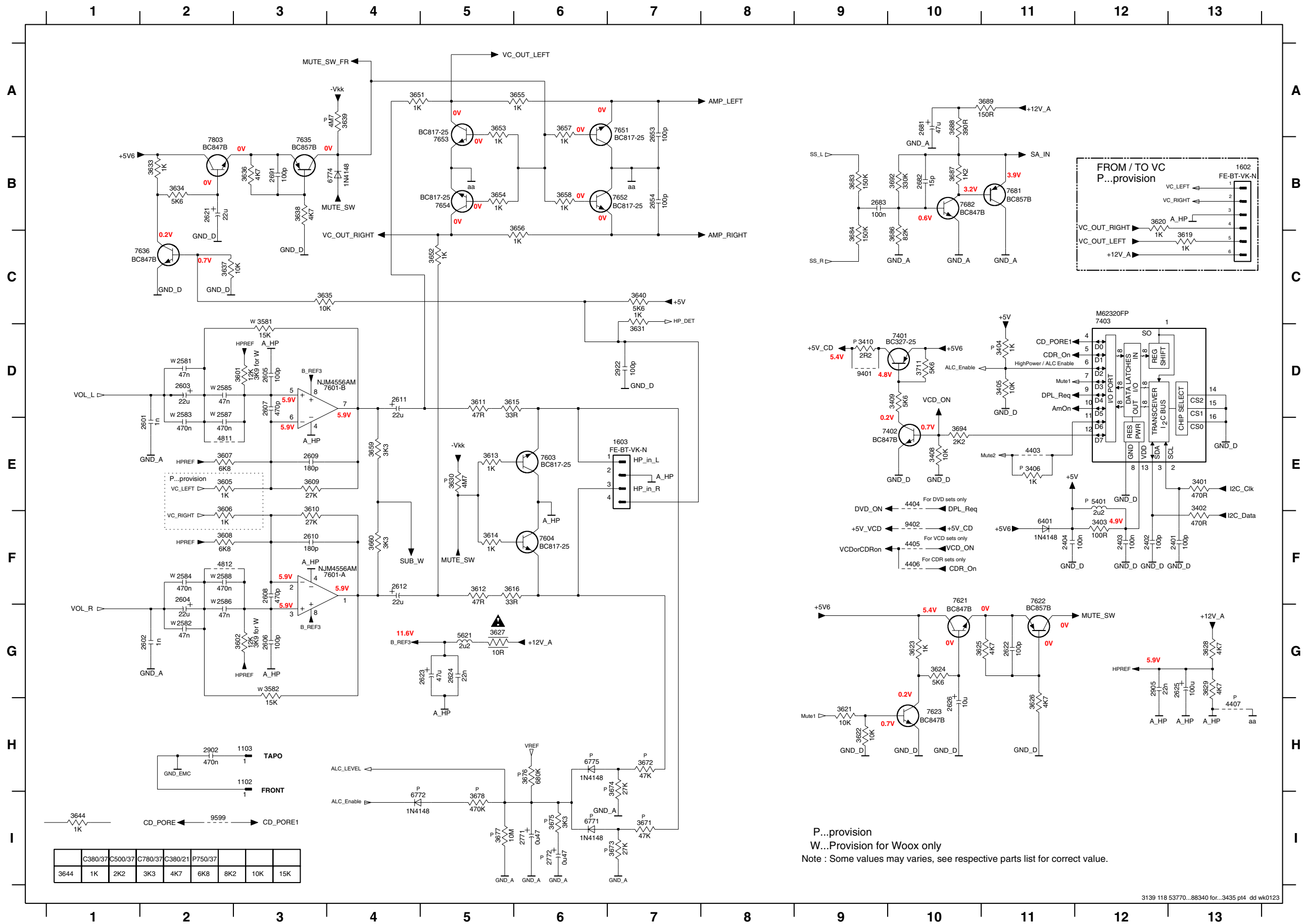


# AF9 BOARD - CIRCUIT DIAGRAM (PART 1)



1501 E1	3511 A3
1502 E3	3512 B3
1503 F1	3513 A3
1504 H1	3514 B3
1505 E4	3521 A3
1506 A1	3522 A3
1510 F1	3523 A3
1531 C1	3524 A3
1532 C2	3525 E6
1691 H5	3526 E7
2201 B7	3531 C2
2202 C7	3532 C3
2203 D7	3533 B3
2204 C6	3534 C3
2205 C5	3541 A7
2206 C7	3542 I7
2501 E2	3543 A9
2502 E2	3544 I9
2503 D3	3545 B9
2504 E3	3546 G9
2505 F5	3547 C9
2506 F7	3548 F9
2507 E7	3549 B9
2511 A4	3550 G9
2512 B4	3551 C11
2513 A4	3552 F11
2514 B4	3553 C11
2515 F6	3554 F11
2516 F7	3555 C12
2521 A4	3556 F12
2522 A4	3557 C12
2523 F6	3558 G12
2524 F6	3559 C14
2531 D4	3560 F14
2532 C4	3561 B14
2533 F6	3562 F14
2534 F7	3563 A12
2535 B3	3564 G11
2536 C3	3565 B12
2541 A8	3566 G11
2542 H8	3567 B12
2543 G9	3568 H11
2544 C9	3569 B13
2546 F9	3570 H11
2547 B10	3571 B13
2548 G10	3572 G13
2549 C11	3591 A10
2550 F11	3592 I10
2551 C11	3593 A10
2552 F11	3594 I10
2553 B12	3641 D14
2554 G11	3642 E14
2555 B12	3643 D12
2556 H11	3645 D11
2557 B13	3646 E11
2558 G12	3661 G3
2559 C12	3662 I3
2560 G12	3663 G3
2561 C14	3664 H3
2562 G14	3665 G1
2563 C14	3666 H1
2564 G14	3667 G3
2565 C9	3668 I3
2567 B9	3669 I3
2568 G9	3690 H7
2589 A10	3699 G6
2590 I9	3700 H6
2591 A10	3707 E12
2592 I10	3708 E12
2593 A11	3712 I3
2594 I11	4501 F5
2641 D13	4641 D14
2642 E13	4642 E14
2661 G2	4813 G5
2662 I2	4920 E1
2663 H2	5403 A1
2664 I2	5404 B1
2665 H2	6201 C6
2666 H2	6202 C6
2667 G1	6641 D13
2669 E1	6642 E13
2696 G7	7201 B6
2697 H7	7488 C14
2698 G5	7489 G14
2707 E11	7501 C8
2708 E12	7585 B13
2904 G1	7586 G11
2908 E12	7661 G2
2921 G5	7662 H2
2923 B1	7663 H3
2924 F5	7693 H6
3201 B6	9618 A1
3202 C6	9654 B1
3435 D3	
3436 D3	
3437 D3	
3438 D3	
3501 D1	
3502 E1	
3503 D2	
3504 E2	
3505 E2	
3506 E2	
3507 E3	
3508 E3	

# AF9 BOARD - CIRCUIT DIAGRAM (PART 2)

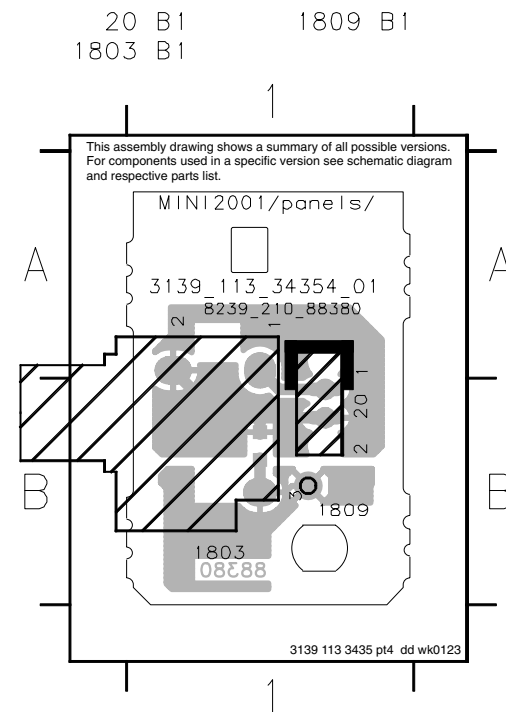


- 1102 H3
- 1103 H3
- 1602 B13
- 1603 E7
- 2401 F13
- 2402 F12
- 2403 F12
- 2404 F11
- 2581 D2
- 2582 G2
- 2583 D2
- 2584 F2
- 2585 D2
- 2586 F2
- 2587 D2
- 2588 F2
- 2601 E2
- 2602 G2
- 2603 D2
- 2604 F2
- 2605 D3
- 2606 G3
- 2607 D3
- 2608 F3
- 2609 E3
- 2610 F3
- 2611 D4
- 2612 F4
- 2621 B2
- 2622 G11
- 2623 G5
- 2624 G5
- 2625 G13
- 2626 H10
- 2653 A7
- 2654 B7
- 2681 A10
- 2682 B10
- 2683 B9
- 2691 B3
- 2771 I6
- 2772 I6
- 2902 H2
- 2905 G12
- 2922 D7
- 3401 E13
- 3402 E13
- 3403 F12
- 3404 D11
- 3405 D11
- 3406 E11
- 3408 E10
- 3409 D10
- 3410 D9
- 3581 D3
- 3582 G3
- 3601 D3
- 3602 G3
- 3605 E2
- 3606 F2
- 3607 E2
- 3608 F2
- 3609 E3
- 3610 F3
- 3611 D5
- 3612 F5
- 3613 E5
- 3614 F5
- 3615 D5
- 3616 F5
- 3619 C13
- 3620 B12
- 3621 H9
- 3622 H9
- 3623 G10
- 3624 G10
- 3625 G10
- 3626 H11
- 3627 G5
- 3628 G13
- 3629 G13
- 3630 E5
- 3631 D7
- 3633 B2
- 3634 B2
- 3635 C3
- 3636 B3
- 3637 C2
- 3638 B3
- 3639 A4
- 3640 C7
- 3644 I1
- 3651 A4
- 3652 C5
- 3653 A5
- 3654 B5
- 3655 A6
- 3656 C6
- 3657 A6
- 3658 B6
- 3659 E4
- 3660 F4
- 3671 I7
- 3672 H7
- 3673 I7
- 3674 H7
- 3675 I6
- 3676 H6
- 3677 I5
- 3678 I5
- 3683 B9
- 3684 C9
- 3686 C10
- 3687 B10
- 3688 A10
- 3689 A11
- 3692 B10
- 3694 E10
- 3711 D10
- 4403 E11
- 4404 E10
- 4405 F10
- 4406 F10
- 4407 H13
- 4811 E2
- 4812 F2
- 5401 E12
- 5621 G5
- 6401 F11
- 6771 I6
- 6772 I4
- 6774 B4
- 6775 H6
- 7401 D10
- 7402 E10
- 7403 C12
- 7601-A G3
- 7601-B D3
- 7603 E6
- 7604 F6
- 7621 F10
- 7622 F11
- 7623 H10
- 7635 B3
- 7636 C2
- 7651 A7
- 7652 B7
- 7653 B5
- 7654 B5
- 7681 B11
- 7682 B10
- 7803 B2
- 9401 D9
- 9599 I2

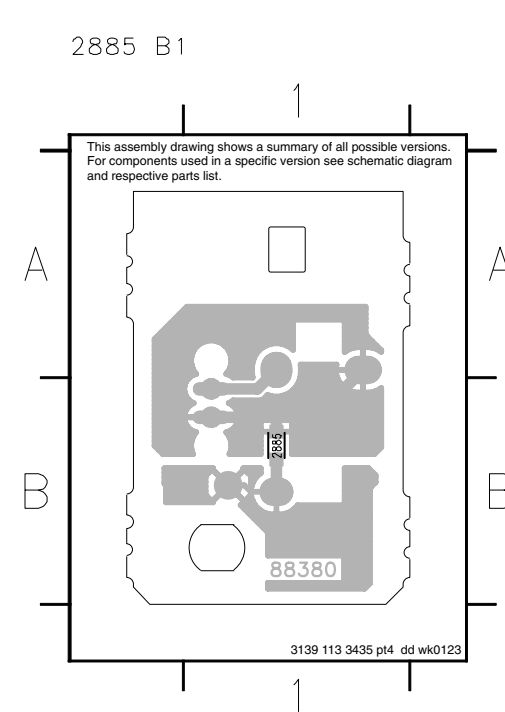
P...provision  
W...Provision for Woox only  
Note : Some values may varies, see respective parts list for correct value.



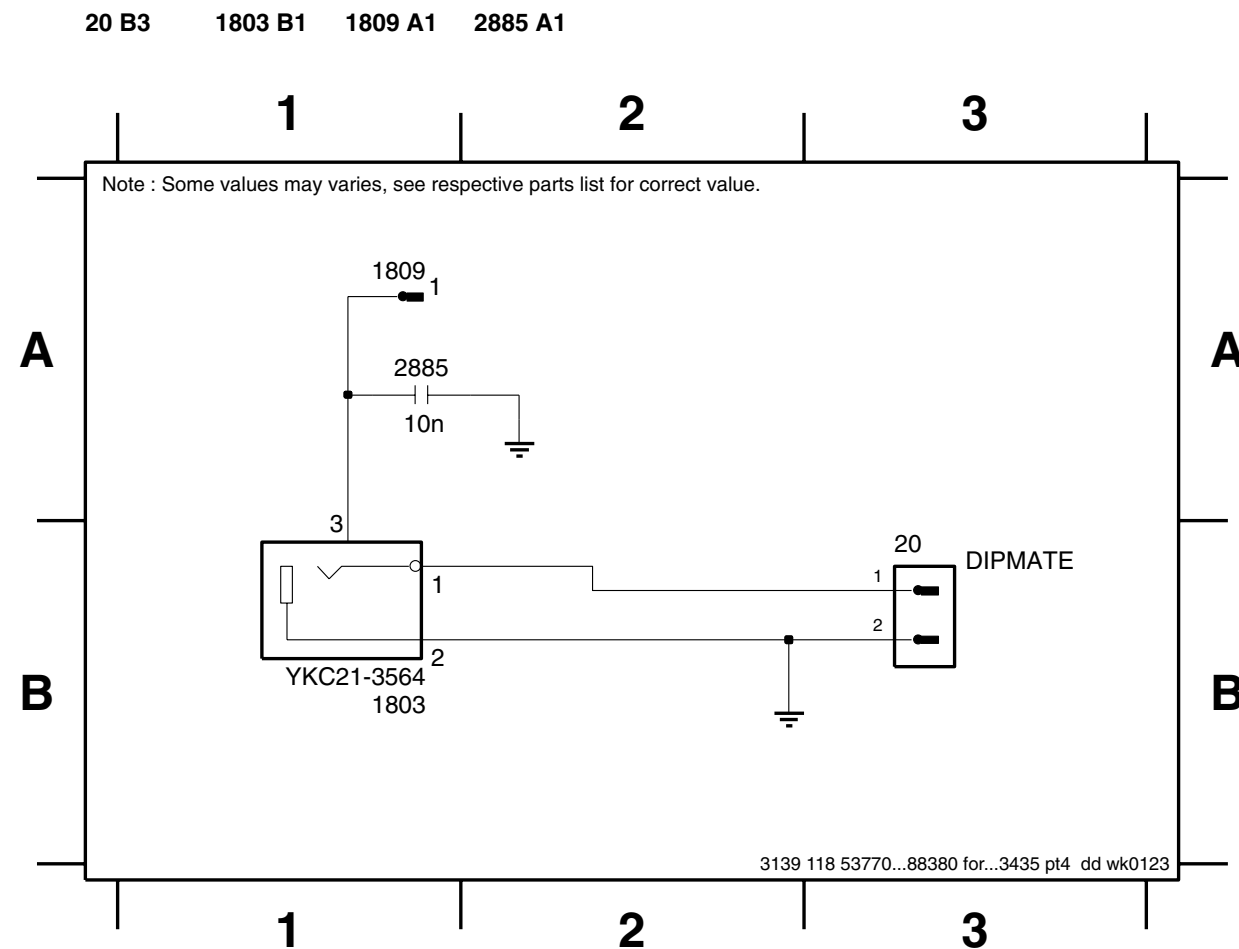
### VIDEO OUT CINCH BOARD - COMPONENT LAYOUT



### VIDEO OUT CINCH BOARD - CHIP LAYOUT



### VIDEO OUT CINCH PART - CIRCUIT DIAGRAM



### TAPE ADJUSTMENT & CHECK TABLE

	TEST CASSETTE	RECORDER MODE	MEASURE ON	READ ON	ADJUST	
					with	to
<b>ADJUST MOTOR SPEED</b>						
NORMAL SPEED	SBC420 3150Hz	PLAY B	1 or 2	frequency counter	3620	3150Hz +/- 0.5%
		PLAY A	LEFT RIGHT		check	3150Hz -0.8/+1.8%
<b>CHECK WOW &amp; FLUTTER</b>						
DECK A & B	SBC420 3150Hz	PLAY	1 or 2	W&F-meter	check	<0.4 % DIN
<b>ADJUST AZIMUTH</b>						
DECK A & B	SBC420 10kHz	PLAY FWD	1 or 2	mV-meter	left hand screw	max. output level & left=right
		PLAY REV #	LEFT RIGHT		right hand screw	
<b>CHECK PLAYBACK FREQUENCY RESPONSE</b>						
DECK A & B	SBC420	PLAY	1 or 2	mV-meter	check	limits see fig.1
<b>ADJUST BIAS CURRENT</b>						
DECK B	SBC419A^	RECORD	5 or 6	mV-meter	3773	995mV
	SBC420				check	750mV +/- 1.5dB
<b>CHECK OVERALL FREQUENCY RESPONSE AND DISTORTION</b>						
Inject 3mV signals 100Hz, 250Hz, 1kHz, 10kHz, 12.5kHz via 3 or 4	SBC419A^ or SBC420	RECORD B				
	RECORDED CASSETTE	PLAY B	1 or 2	mV-meter	check	limits see fig. 2 *
Inject 1kHz 8.85mV via 3 or 4	SBC419A^ or SBC420	RECORD B				
	RECORDED CASSETTE	PLAY B	1 or 2	THD-meter	check	<3% *

SBC419A^ : 4822 397 30069  
SBC420 : 4822 397 30071

# For Auto-reverse version only  
\* If high frequencies are not within limits, decrease bias and re-measure.  
If distortion is too high, increase bias and re-measure  
^ Not applicable for Ferro version

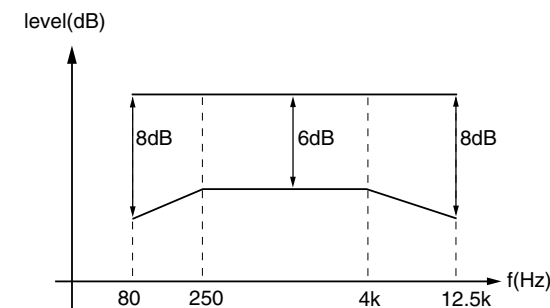


figure. 1

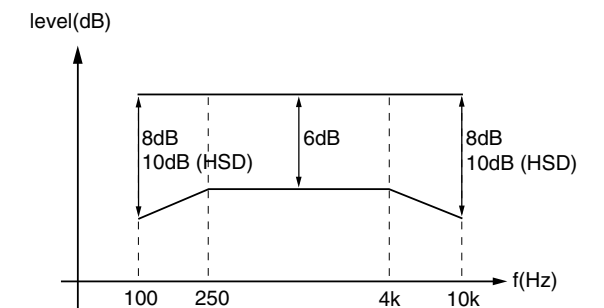
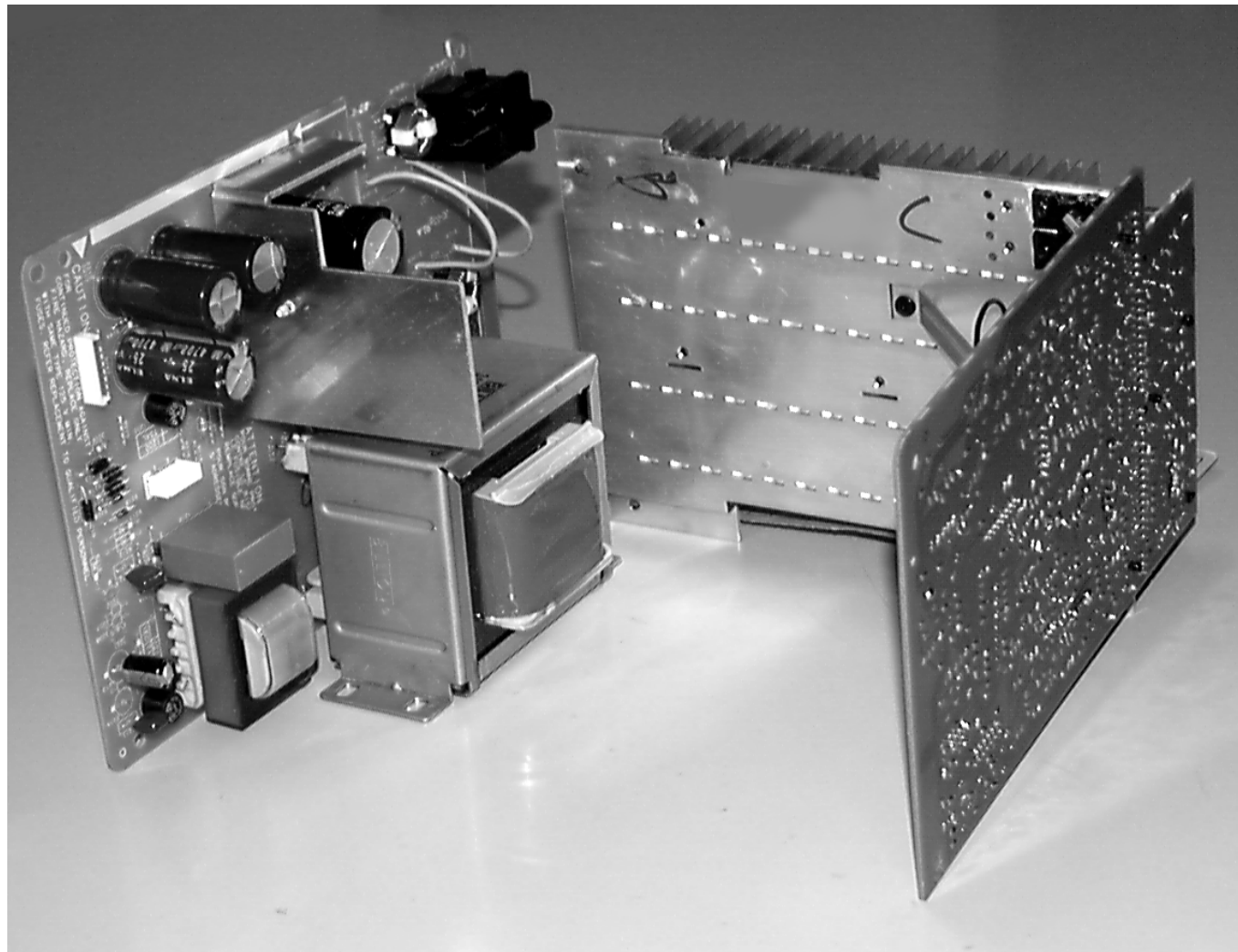


figure. 2





# POWER 2001 Module

(30 - 70W Version)

stage .9

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Circuit Diagram <i>Power part</i> .....	11-7
Partslist .....	11-8

### Circuit details:

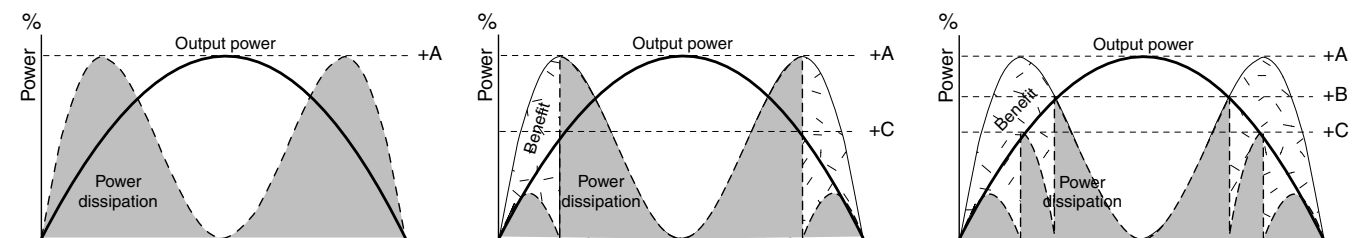
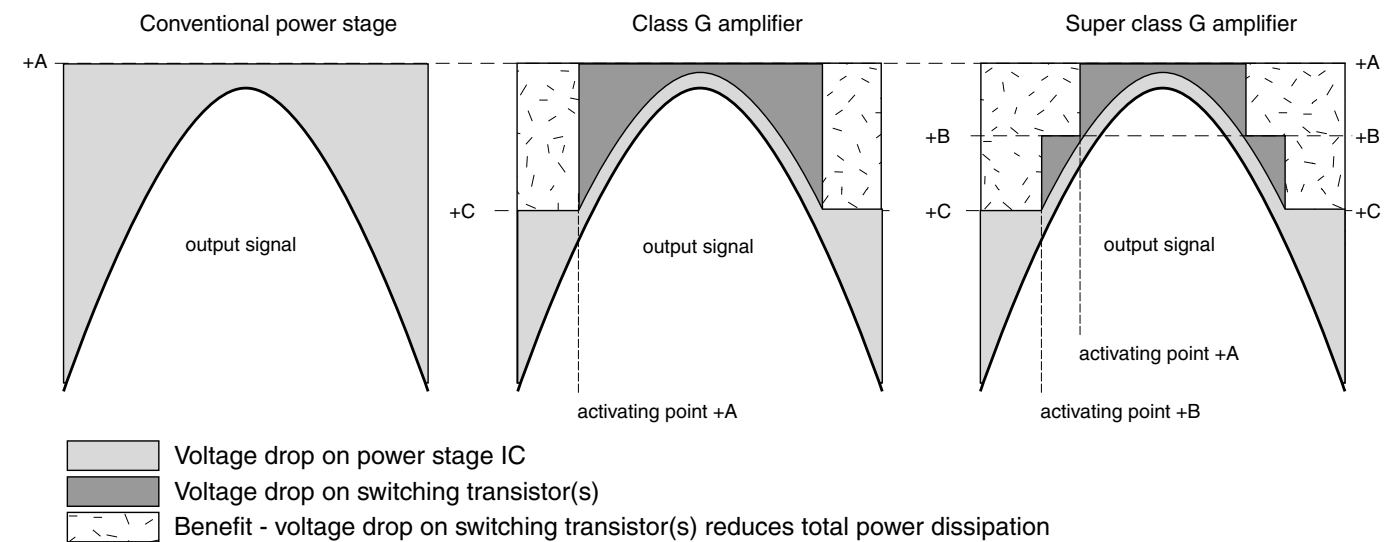
#### Amplifier:

Attention: In the POWER 2001 module the power amplifier IC AN7591 is used as a bridge-amplifier.  
Any connection from output to ground will destroy the output stages!

- Via the AMP\_ON control line, connected to pins 6 (Stby), the power amplifiers are switched on/off by the  $\mu$ P.  
High level (approx. 4,5V): power amplifiers switched on  
Low level (approx. 0V): power amplifiers switched off
- Super class G - operation

The power amplifiers operate as so-called super class G - amplifiers:  
The supply pins 12 (Vcc) are not just connected to one fixed DC-supply as in conventional amplifiers.  
Dependent on the output power there are three different DC-voltages supplied to the power amplifiers:  
⇒ +C1 (+20V) for low output power  
⇒ +B1 (+29V) for medium output power  
⇒ +A1 (+41V) for high output power

### Principle / benefit of Super Class G



**Circuit details continued:**

• **Low power standby feature**

An additional small standby transformer, reduces power consumption in standby-mode. In case power is switched on, the control line ECO is low → relay 1210 is activated → contacts 1 and 2 are closed → transformer 5001 is connected to mains. When the set is switched off (standby) the control line ECO is high → relay 1210 is not activated → main transformer is disconnected. Via standby transformer and rectifiers 6210-6214 the supply voltage LOW\_PWR\_SUP is substituted. This voltage is always available and so the microprocessor is kept running.

• **DC voltages +A1, +B1, +C1**

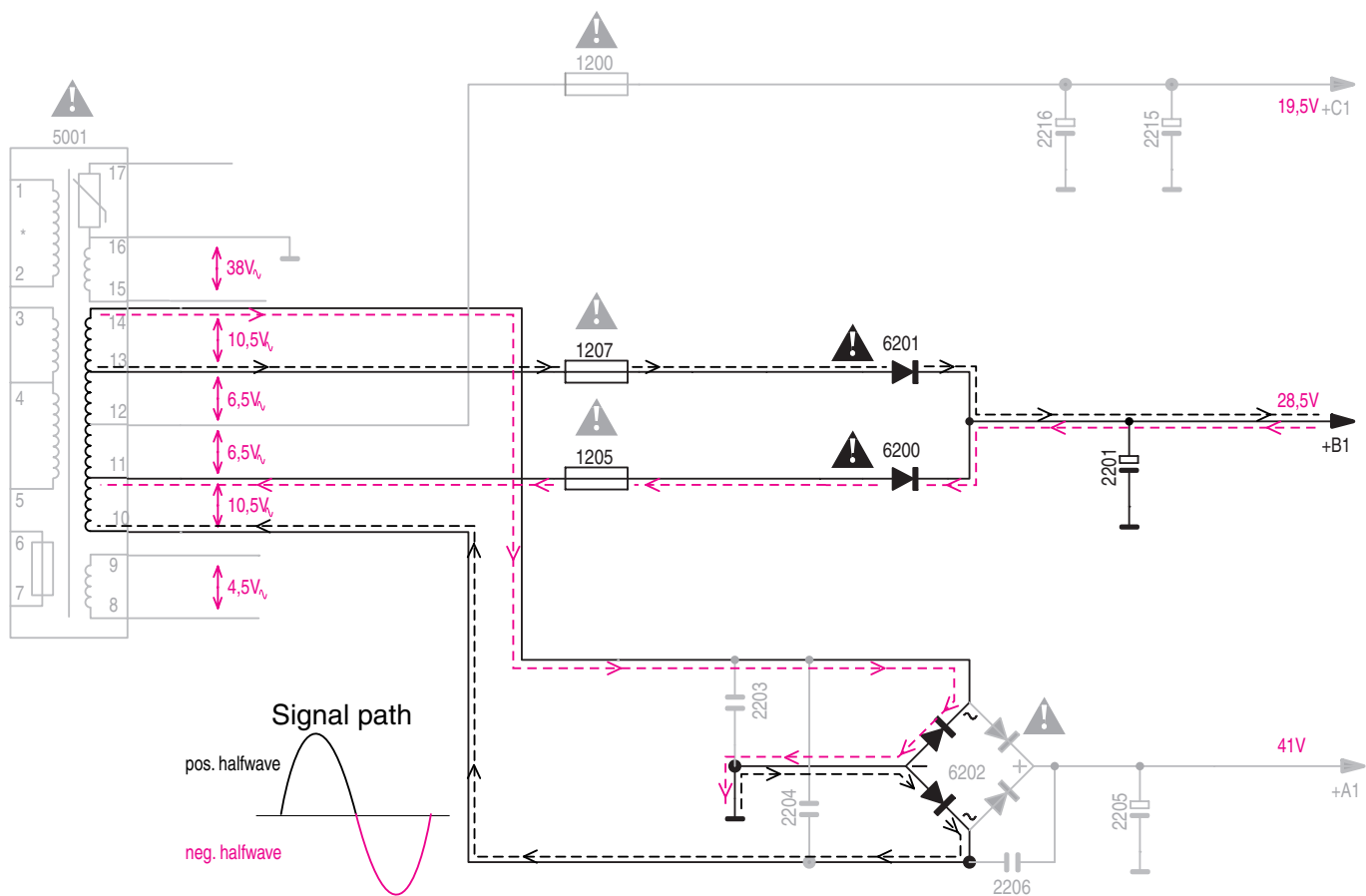
These voltages supply the Super Class G amplifier, described on previous page. The whole power supply is optimized for the special characteristic of this type of amplifier. For that reason several “tricky” details have been applied to ensure optimal efficiency and symmetrical load to the mains transformer.

**Generation of +A1**

Common full wave rectifying with bridge rectifier 6202, using 100% secondary winding of mains transformer (pin 10-14).

**Generation of +B1**

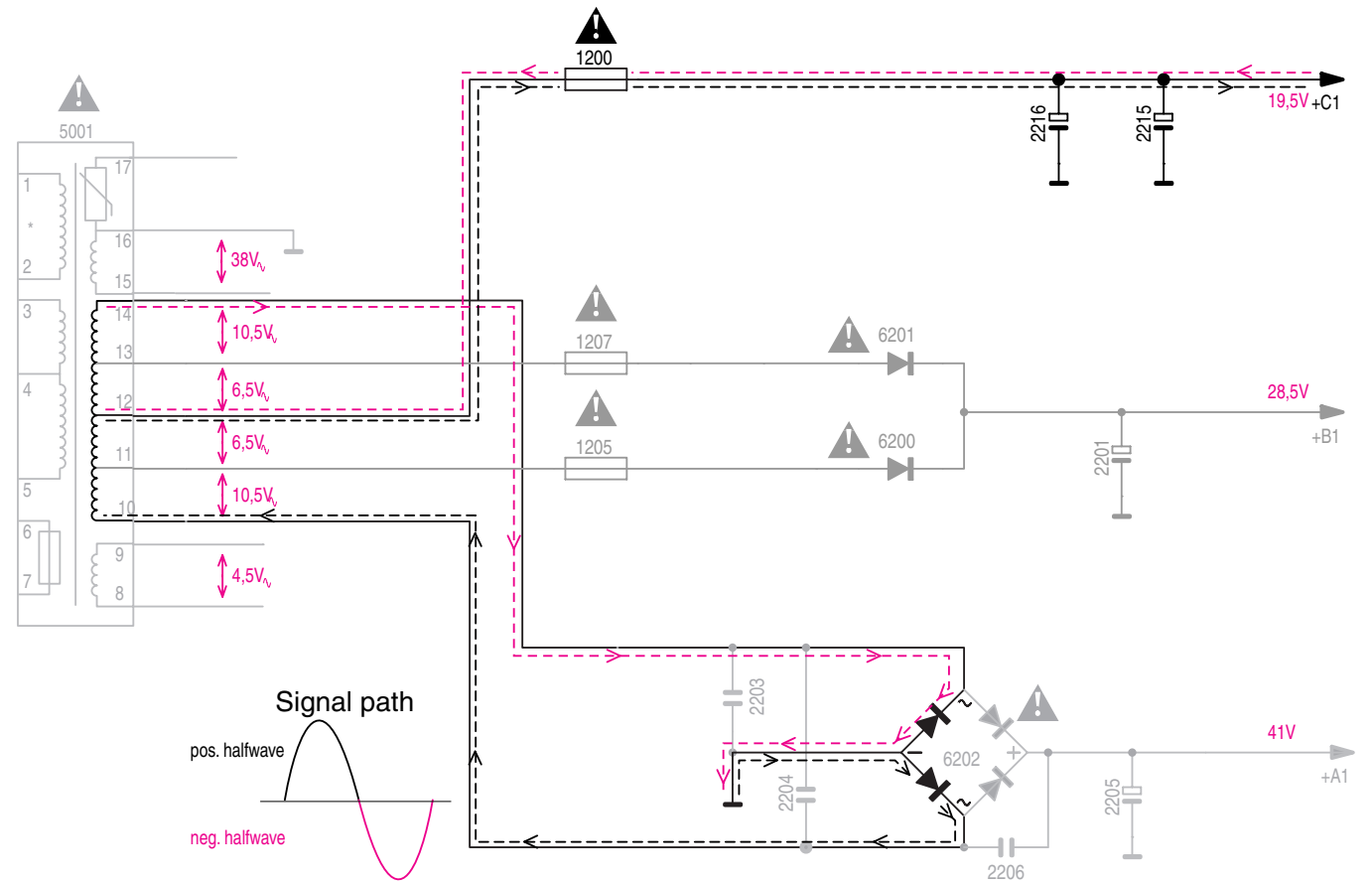
The supply for +B1 consists of one full wave rectifier: – 2 diodes of bridge rectifier 6202, with 6200(6220 in parallel) 6201(6221 in parallel) for generation of +B1 using approx. 70% secondary winding of mains transformer (pin 10-13 respectively pin 11-14). As example for generation of +B1 see picture 1.



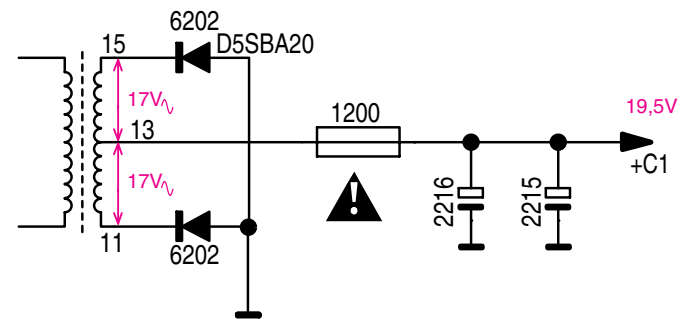
picture 1

**Generation of +C1**

Full wave rectifying with 2 diodes of bridge rectifier 6202, using 50% secondary winding of mains transformer (pin 13-15/13-11). See picture 2 below.

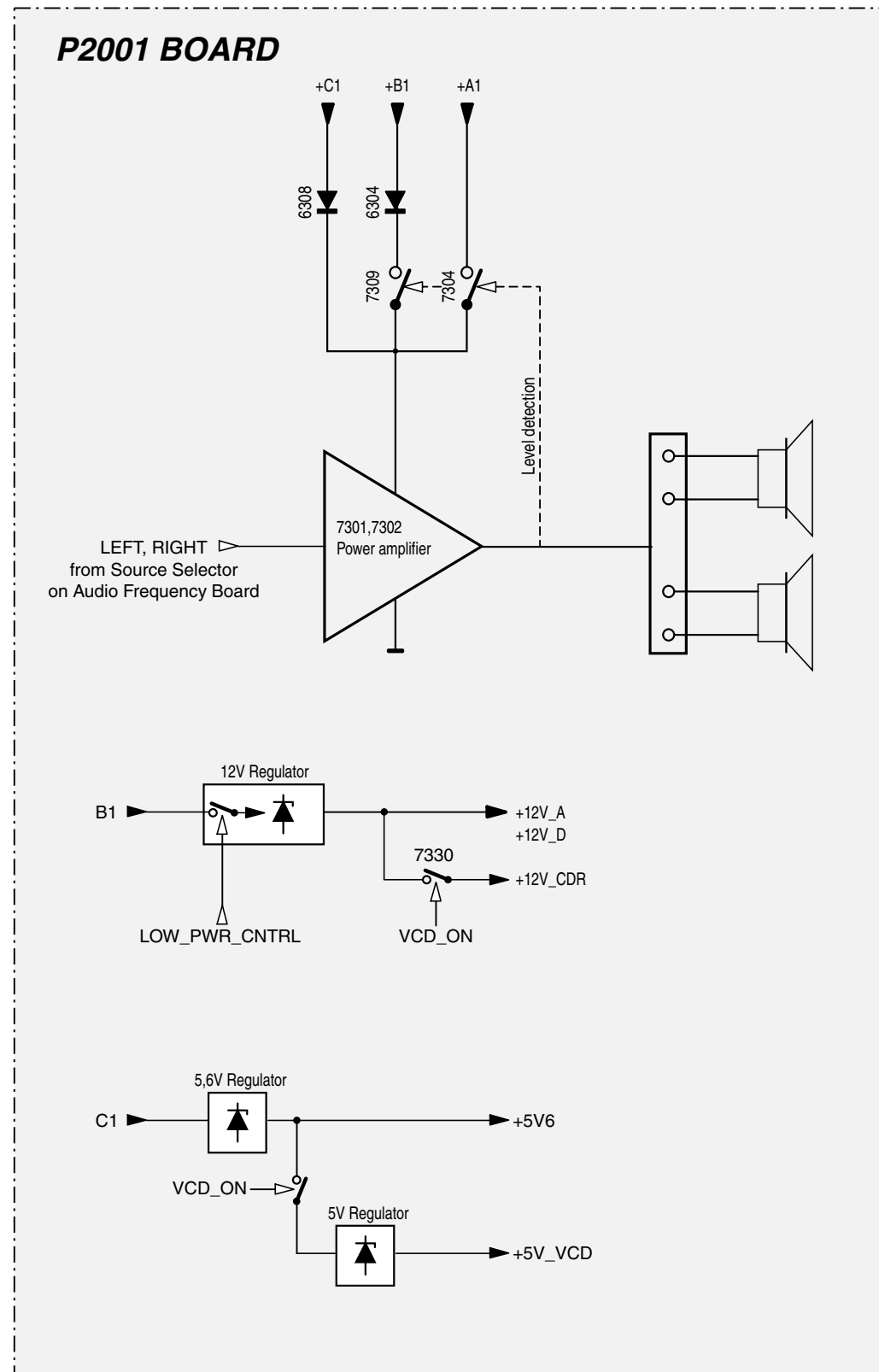
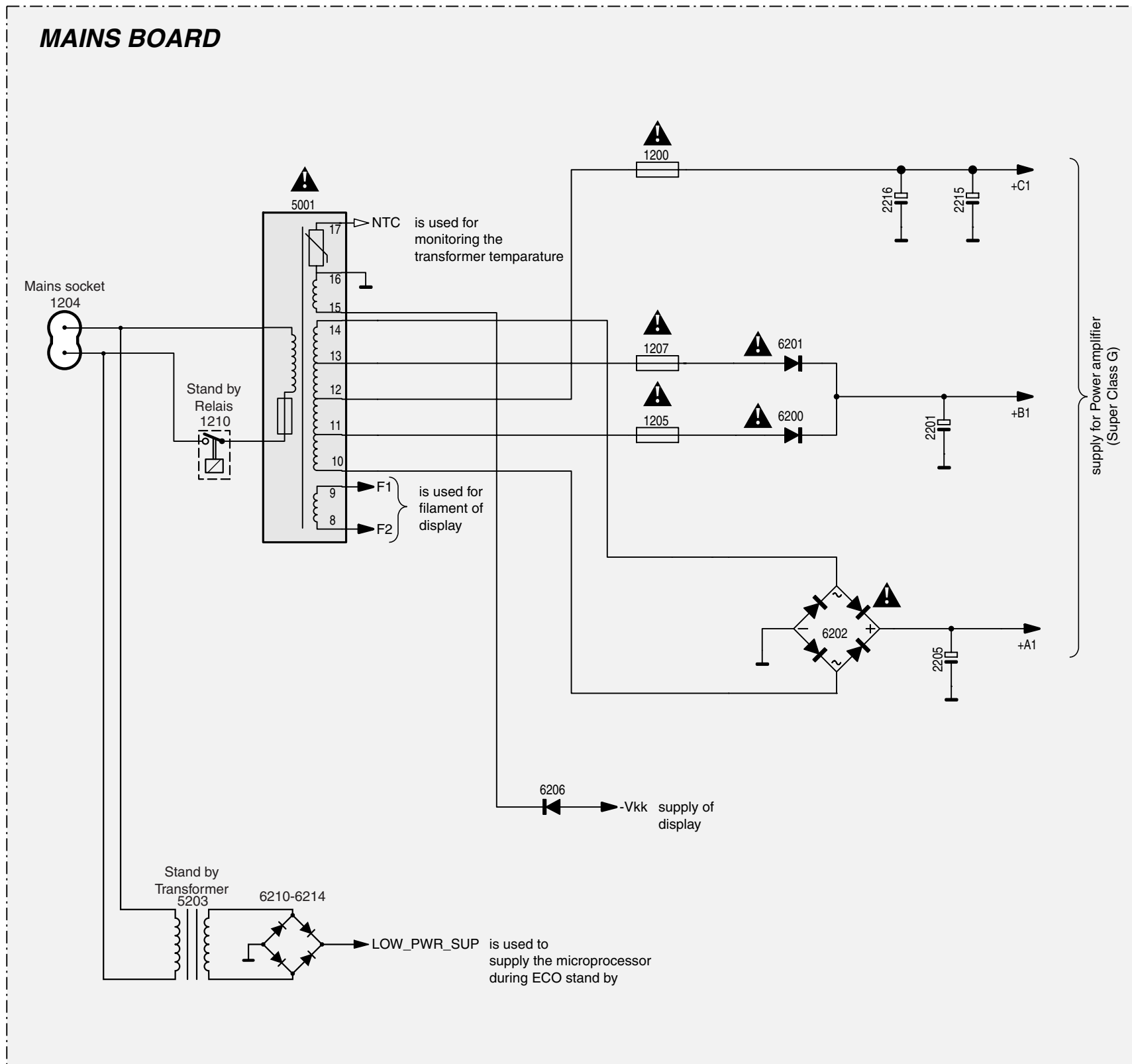


simplified:

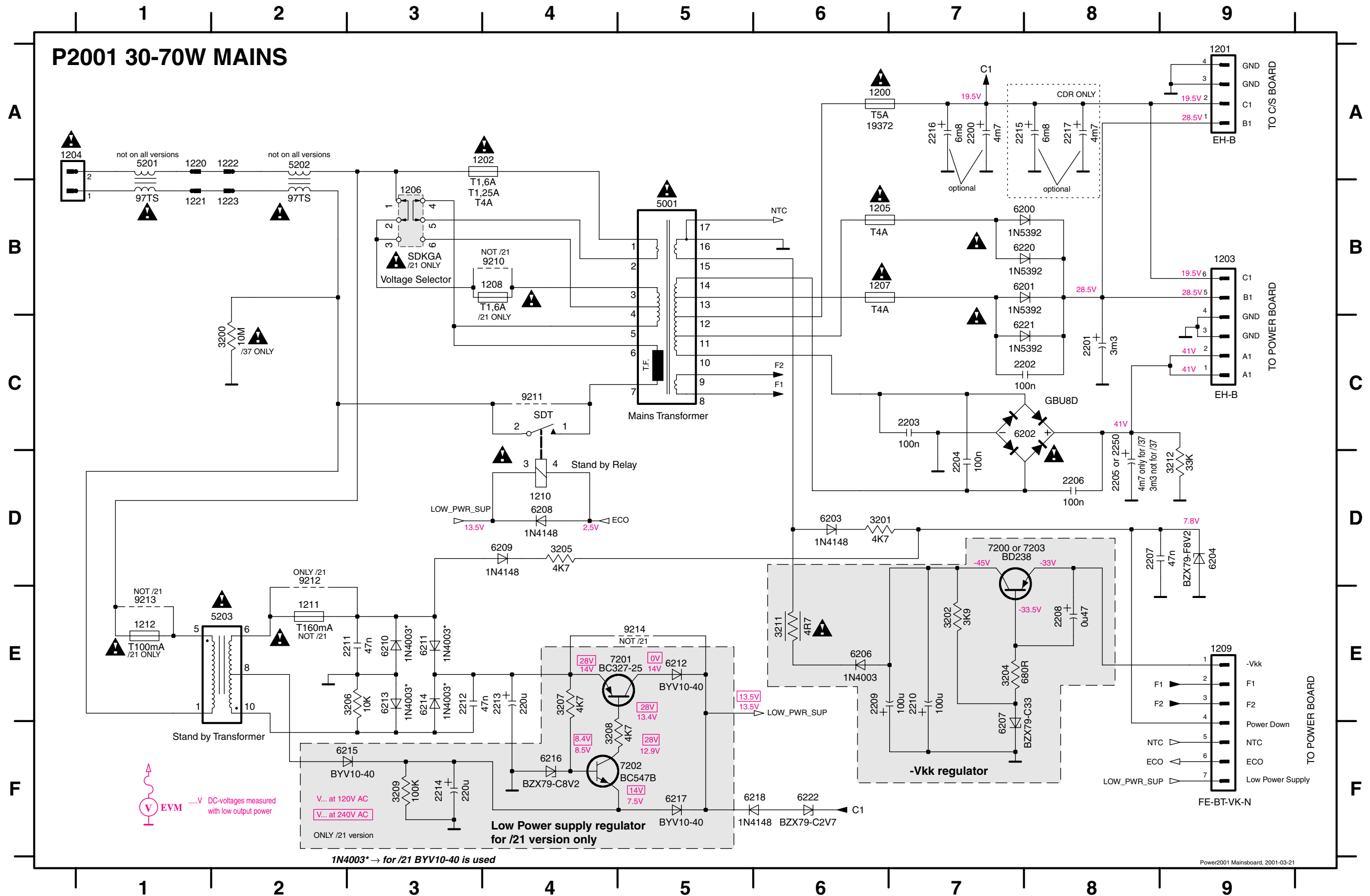


picture 2

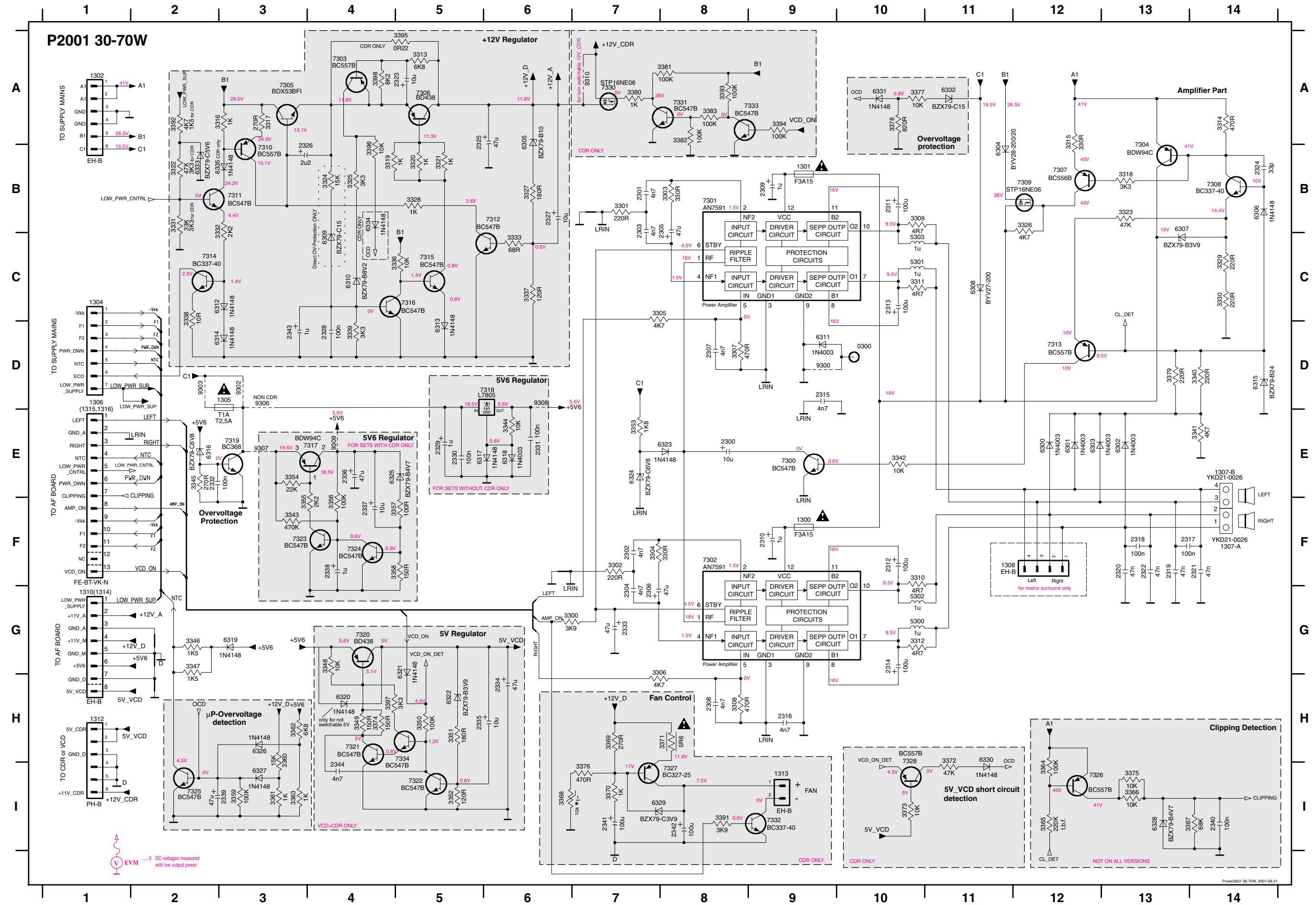
Block Diagram



1200 A6	1207 B6	1222 A2	2204 D7	2210 E7	2216 A7	3204 E7	3211 E6	6201 B8	6208 D4	6214 E3	6221 C8	9208 B2	9215 F6
1201 A9	1208 B4	1223 B2	2205 D8	2211 E3	2217 A8	3205 E4	3212 D9	6202 C8	6209 E4	6215 F3	7200 D7	9210 B4	
1202 A4	1209 E9	2200 A7	2206 D8	2212 E3	2250 D8	3206 E3	5001 C5	6203 D6	6210 E3	6216 F4	7201 E4	9211 C4	
1203 B9	1210 D4	2201 C8	2207 D8	2213 E4	3200 C2	3207 E4	5202 A2	6204 D9	6211 E3	6217 F5	7202 F5	9212 D2	
1205 B6	1211 E2	2202 C8	2208 E8	2214 F3	3201 D6	3208 F4	5203 E1	6206 E6	6212 E5	6218 F6	7203 D7	9213 E1	
1206 B3	1212 E1	2203 C7	2209 E6	2215 A7	3202 E7	3209 F3	6200 B8	6207 F7	6213 E3	6220 B8	9206 A2	9214 E5	



0300	D10	1307-a	F14	1315	D1	2305	B8	2312	F10	2319	F13	2326	B3	2333	G7	2340	I14	3303	B8	3310	F10	3317	A3	3324	B4	3331	B2	3340	D14	3347	G2	3354	E3	3361	I3	3368	I6	3375	I13	3382	A8	6300	E12	6307	B13	6314	D2	6321	H5	6328	I13	7302	F8	7309	B12	7316	C5	7323	F3	7331	A8	9306	D4
1300	F9	1307-b	E14	1316	D1	2306	G7	2313	C10	2320	F13	2327	B6	2334	H6	2341	I7	3304	F7	3311	C10	3318	B13	3325	B4	3332	B3	3341	E10	3348	G4	3355	F3	3362	I3	3369	H7	3376	I7	3383	A8	6301	E12	6308	C11	6315	D14	6322	H5	6329	I7	7303	A4	7310	B3	7317	E3	7324	F4	7332	F4	9307	D4
1301	B9	1308	F12	2300	E8	2307	D8	2314	G10	2321	F13	2328	D4	2335	H5	2342	I8	3305	C7	3312	G10	3319	B4	3326	B12	3333	C6	3342	E10	3349	H4	3356	F4	3363	I3	3370	I7	3377	A10	3384	A8	6302	E13	6309	C4	6316	F5	6323	E8	6330	H11	7304	A3	7311	B3	7318	D6	7325	I2	9300	D9	9310	A5
1302	A1	1310	G1	2301	B7	2308	H8	2315	D9	2322	F13	2329	E5	2336	E4	2343	D3	3306	H7	3313	A5	3320	B6	3327	C6	3334	C5	3343	F3	3350	H7	3357	F3	3364	I12	3371	H8	3378	A10	3385	A7	6303	E12	6310	C4	6317	E6	6324	E7	6331	A10	7305	A8	7312	B6	7319	F6	7326	I12	9302	D3		
1304	C1	1312	H1	2302	F7	2309	B9	2316	H9	2323	A4	2330	E5	2337	F4	2344	B4	3307	D8	3314	A14	3321	B5	3328	B3	3335	C6	3344	E5	3351	H5	3358	F5	3365	I12	3372	I11	3379	D13	3386	C10	6304	B11	6311	D9	6318	E6	6325	E5	6332	A11	7306	A5	7313	D12	7320	G4	7327	H4	9303	D2		
1305	D3	1313	I9	2303	B7	2310	F9	2317	F13	2324	A4	2331	E6	2338	F4	2345	B2	3308	H8	3315	A12	3322	B2	3329	C14	3336	C2	3345	F5	3352	I5	3359	I3	3366	I13	3373	I10	3380	A7	5301	G10	6305	A6	6312	C2	6319	G3	6326	H2	6333	E2	7307	B12	7314	C2	7321	H4	7328	H10	9306	D3		
1306	E1	1314	G1	2304	G7	2311	B10	2318	F13	2325	A5	2332	F5	2339	I2	2346	F7	3309	B10	3316	A3	3323	B13	3330	C14	3337	D4	3346	G2	3353	E7	3360	H3	3367	I14	3374	H4	3381	A8	5303	C10	6306	B14	6313	D5	6320	H4	6327	H3	7301	B8	7308	B14	7315	C5	7322	I5	7330	A7	9307	E3		



# SET BLOCK DIAGRAM

